

### **Remarks**

Applicant thanks the Examiner for the careful examination of this application and the clear explanation of the rejections.

The new title conforms to the amended independent claim. The new abstract is in proper form and conforms to the disclosure.

Specification paragraph [0003] now includes the substance of the original Abstract.

The cancelled claims will be presented in divisional patent applications.

Amended claim 1 better describes the subject matter the applicant regards as his invention.

Independent claim 1 now defines a particular test architecture comprising a plurality of core wrappers, input circuitry, output circuitry, and a link instruction register. The core wrappers, the input circuitry, and the output circuitry have specific described limitations.

The link instruction register also has specific claimed limitations to interface with the specifics of the core wrappers. These include: having a serial input, a serial output, control inputs, and control outputs; the control inputs being coupled to those of the core wrappers; control outputs including plural enable outputs, each enable output being coupled to the enable input of a core wrapper; and the serial input and the serial output of the link instruction register being connected in series with the serial input of the input circuitry and the serial output of the output circuitry.

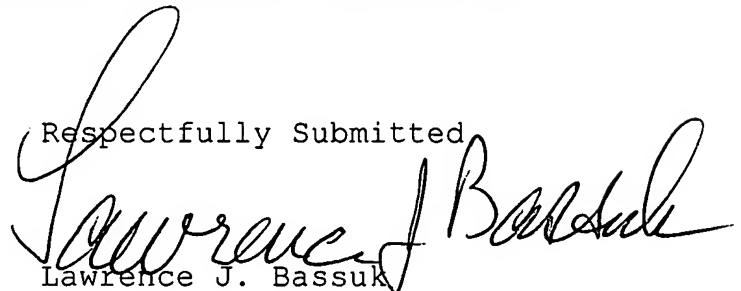
The link instruction register includes an instruction register connected to the serial input and to the control outputs, and a multiplexer connected to the serial input, the serial output, and the select input.

In contrast, US 5,889,788 to Pressly discloses plural speed path test cells 16 and 18, each connected in series with an input signal line or an output signal line between an embedded core 14 and customer specified logic 12. The test cells are connected to a test ring scan data input TRSDI and a test ring scan data output TRSDO. The embedded core 14 includes scan paths SDI/O 1 and SDI/O 2. The speed cells are used for speed testing of the embedded core 14.

Present claim 1 requires the described link instruction register. This distinguishes over the disclosure in the Pressly patent.

The application is in allowable form and the claims distinguish over the cited references. Applicant respectfully requests reconsideration or further examination of this application.

Respectfully Submitted

  
Lawrence J. Bassuk  
Reg. No. 29,043  
Attorney for Applicant

Texas Instruments Incorporated  
P. O. Box 655474, MS 3999  
Dallas, Texas 75265  
972-917-5458